

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Canada

72,915	A. J. Kerklaan	Clam Shell Cover for IC Memory Cards
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France

265,573	A. M. Derouault and B. Merialdo	An Automatic Stenotypy to French Transcription System
331,858	C. Galand and M. Rosso	Multi-rate CELP/LTP
339,166	P. Debord and R. Glaise	Correcting a Soft Error in Presence of Multiple Hard Ones
346,555	B. Naudin, C. Pin, E. Saint-Georges, J. Calvignac, and J. Feraud	Parallel Bit Processing for Efficient HDLC Scanning
365,732	D. Omet and S. Leforestier	Two Stage Address Decoder Circuit for Semiconductor Memories
373,277	A. Milewski and T. Liethoudt	Multifrequency Modem Using Trellis Coded Modulation
378,037	D. Pilost, J. Belloc, and M. Quintin	Modem Having a Software-Adapted Modulation Rate

Germany

59,002,405.1	N. Urban	Optical Storage with Random Access
3,786,549,808	J. Bartha, J. Greschner, J. Wittlinger, J. W. Bartha, and T. Bayer	Process for Fabricating Arbitrarily Shaped Through Holes in a Component

United Kingdom

249,696	G. M. Trees	A Multiple Window Display System
250,157	J. A. Herrod, M. W. Ricketts, and T. Anthias	Data Display System
265,614	P. Beanlands	Cathode Ray Tube Apparatus
288,692	R. Pascual	Graphics Input Tablet
288,720	R. W. Farr	Curve Generation in a Display System
2,029,918	K. MacMurtrie, M. H. Sharp, and P. M. Alfonso	Appearance Design for Rack Drawer Front Covers
2,030,330	J. A. Wiseman, M. D. Oates, R. Sapper, T. Imai, and T. Takahashi	Saturn 14" and 15" Monitors
2,030,451	T. J. Rodd	Flat Panel Visual Display Unit

United States

5,193,190	J. M. Janczyn and P. W. Markstein	Partitioning Optimizations in an Optimizing Compiler
5,193,732	M. J. Interrante, M. Berger, E. F. Handford, and E. Tas	Apparatus and Methods for Making Simultaneous Electrical Connections
5,194,196	D. A. Chance, D. B. Goland, and H.-M. Tong	Hermetic Package for an Electronic Device and Method of Manufacturing Same

5,194,397	R. K. Cook, R. W. Knepper, S. K. Kulkarni, R. C. Lange, P. A. Ronsheim, S. Subbanna, M. J. Tejwani, and B. H. Yun	Method for Controlling Interfacial Oxide at a Polycrystalline/Monocrystalline Silicon Interface
5,194,684	R. J. Lisle and B. S. McDonald	Method and Apparatus for Selective Reduction of Upper Harmonic Content in Digital Synthesizer Excitation Signals
5,194,713	F. D. Egitto, C. J. Moring, and D. C. Van Hart	Removal of Excimer Laser Debris Using Carbon Dioxide Laser
5,194,863	J. H. Barker, E. LeFranc, and D. D. Williams	Touch Panel Display
5,194,928	J. E. Cronin, P. A. Farrar, Sr., H. G. Linde, and R. A. Previti-Kelly	Passivation of Metal in Metal/Polyimide Structure
5,194,930	K. Papathomas, M. D. Poliks, D. W. Wang, and F. R. Christie	Dielectric Composition and Solder Interconnection Structure for Its Use
5,195,034	D. Garneau and W.-H. Sears	Method for Quasi-Key Search within a National Language Support (NLS) Data Processing System
5,195,052	F. O. Karim	Circuit and Method for Performing Integer Power Operations
5,195,097	R. Bogholtz, Jr., L. J. Bosch, and T. H. Mitchell, Jr.	High Speed Tester
5,195,167	L. R. Bahl, P. V. De Souza, P. S. Gopalakrishnan, D. Nahamoo, and M. A. Picheny	Apparatus and Method of Grouping Utterances of a Phoneme into Context-Dependent Categories Based on Sound-Similarity for Automatic Speech Recognition
5,195,185	G. B. Marenin	Dynamic Bus Arbitration with Concurrent Same Bus Granting Every Cycle
5,196,070	W. H. Ables, D. R. Howle, J. S. Humphrey, R. G. Raines, R. A. Reich, R. A. Sorensen, R. Valdez, and A. R. Zamani	Thermally Stable Water Soluble Solder Flux and Paste
5,196,096	I.-C. H. Chang and W. J. Horkans	Method for Analyzing the Addition Agents in Solutions for Electroplating of PBSN Alloys
5,196,251	N. Bakhru, R. A. Bates, G. Czornyj, N. DiPaolo, A. H. Kumar, S. Mukkavilli, H. O. Steimel, and R. R. Tummala	Ceramic Substrate Having a Protective Coating Thereon and a Method for Protecting a Ceramic Substrate
5,196,701	J. S. Foster, K. A. Rubin, and J. K. Spong	High-Resolution Detection of Material Property Variations
5,196,706	J. H. Keller and J. W. Robinson	Extractor and Deceleration Lens for Ion Beam Deposition Apparatus
5,196,722	A. S. Bergendahl, C. L. Bertin, J. E. Cronin, H. L. Kalter, D. M. Kenney, C. H. Lam, and H.-S. Lee	Shadow RAM Cell Having a Shallow Trench EEPROM
5,196,734	B.-H. Han	CCS Latch with In-Circuit Redundancy
5,196,835	L. A. Blue, B. T. Freedman, J. F. Gonzalez-Heres, and A. G. MacInnis	Laser Touch Panel Reflective Surface Aberration Cancelling
5,196,836	D. D. Williams	Touch Panel Display
5,196,840	V. S. Leith and J. V. Stevens	Secure Communications System for Remotely Located Computers
5,196,849	R. L. Galbraith	Method and Apparatus for Implementing PRML Codes with Maximum Ones
5,196,853	J.-C. Abbiate and G. Richter	Sigma Delta Converter Insensitive to Asymmetrical Switching Times
5,196,924	L. Lumelsky, C. B. Swart, J. L. Pittas, S. M. Choi, and A. W. Peevers	Look-Up Table Based Gamma and Inverse Gamma Correction for High-Resolution Frame Buffers
5,196,934	R. M. Fleischman and B. J. Wilkie	Image Scaling Apparatus for a Multimedia System
5,196,997	J. M. Kurtzberg and M. Levanoni	Method and Apparatus for Quality Measure Driven Process Control
5,197,000	J. P. Vincent	Method of Scheduling Meetings
5,197,001	S. K. Mukherjee	Bill of Material and Project Network Processing
5,197,055	J. C. Hartung, D. L. Patton, D. L. Willson, and D. J. Winarski	Idle Demount in an Automated Storage Library
5,197,065	J. Calvignac, J. Feraud, J.-P. Lips, B. Naudin, and E. Saint-George	Distribution Mechanism for Establishing Communications between User Interfaces of a Communication System
5,197,069	T. E. Cook, M. J. Fantacone, I. Hatanaka, and H. M. Yudenfriend	Method and System for Detecting and Recovering from Switching Errors
5,197,086	F. E. Jackson, B. J. Letner, and N. T. Nguyen	High Speed Digital Clock Synchronizer
5,199,163	S. G. Ehrenberg and J. L. Poetzinger	Metal Transfer Layers for Parallel Processing
5,199,799	Y. Masuda, M. Sasaki, and K. Satoh	Ball Bearing and Head Positioning Apparatus
5,199,879	H. Kohn, K. V. Desai, R. J. Romanosky, G. J. Saxenmeyer, Jr., R. E. Tomek, and J. R. Webb	Electrical Assembly with Flexible Circuit
5,199,892	J. S. Campbell, F. W. Doolittle, D. B. Howe, and C. S. Pendleton	Connector Assembly and Information Handling System Component Utilizing Same
5,200,026	S. Okabe	Manufacturing Method for Multi-Layer Circuit Boards

5,200,112	M. Angelopoulos, W.-S. Huang, R. D. Kaplan, M.-A. Le Corre, S. E. Perreault, J. M. Shaw, M. R. Tissier, and G. F. Walker	Electrically Conductive Polymeric Materials and Uses Thereof
5,200,631	F. D. Austin, R. Kachmarik, and L. T. Olson	High Speed Optical Interconnect
5,200,677	G. Dueck and U. Schauer	Method and Apparatus for Controlling Positioning Means
5,200,864	E. C. Dunn, S. M. Fry, S. A. Jackson, N. H. MacLean, Jr., R. P. Reynolds, and R. A. Ripberger	Combining Small Records into a Single Record Block for Recording on a Record Media
5,200,867	T. R. Albrecht, J. S. Foster, and A. M. Homola	Transducer Carrier for Disk File with Liquid Film Head-Disk Interface
5,200,936	R. Allenspach and A. Bischof	Magneto-Optic Method and Apparatus for Recording and Retrieving High-Density Digital Data
5,200,945	E. M. Engler, S. L. Heesacker, D. A. Holmstrom, and B. S. Lee	Optical Data Storage System with Airflow Defecting Member
5,200,999	S. M. Matyas, D. B. Johnson, A. V. Le, R. Prymak, W. C. Martin, W. S. Rohland, and J. D. Wilkins	Public Key Cryptosystem Key Management Based on Control Vectors
5,201,000	S. M. Matyas, D. B. Johnson, A. V. Le, R. Prymak, and J. D. Wilkins	Method for Generating Public and Private Key Pairs without Using a Passphrase
5,201,033	S. T. Eagen and H. G. Kiel	Method for Controlling Cursor Movements on Certain Computer Workstations
5,201,041	J. E. Bohner, T. T. Do, R. J. Gusefski, K. C. Huang, and C. I. Lei	Cache Bypass Apparatus
5,201,044	A. H. Frey, Jr., R. C. Mosteller, J. M. Gould, N. R. Mendelsohn, and J. Perchik	Data Processing Method for File Status Recovery Includes Providing a Log File of Atomic Transactions that May Span both Volatile and Non Volatile Memory
5,201,047	R. A. Maki and S. K. Mukherjee	Attribute-Based Classification and Retrieval System
5,201,049	D. U. Shorter	System for Executing Applications Program Concurrently/Serially on Different Virtual Machines
5,201,053	M. T. Benhase, F. J. Clark, and W. T. Higgins	Dynamic Polling of Devices for Nonsynchronous Channel Connection
5,201,451	K. V. Desai, N. P. Franchak, R. H. Katyl, H. Kohn, T. A. Sholtes, V. G. Veeraraghavan, and C. G. Woychik	Method and Apparatus for Mounting a Flexible Film Semiconductor Chip Carrier on a Circuitized Substrate
5,201,866	L. S. Mok	Structure for Dissipation of Heat Having Slidably Engaged Fins for Conformal Disposition against a Heat Generating Surface
5,202,061	M. Angelopoulos, W.-S. Huang, R. D. Kaplan, M.-A. Le Corre, S. E. Perreault, J. M. Shaw, M. R. Tissier, and G. F. Walker	Electrically Conductive Polymeric Materials and Uses Thereof
5,202,197	D. F. Ansell and H. T. Foy	Data Processing Device Having an Improved Manually Operated Battery Eject Mechanism and a Warning Light
5,202,272	C.-M. Hsieh, L. L. Hsu, S. A. Kumar, and Z.-J. Tien	Field Effect Transistor Formed with Deep-Submicron Gate
5,202,287	R. V. Joshi, C.-S. Oh, and D. Moy	Method for a Two Step Selective Deposition of Refractory Metals Utilizing SiH_4 Reduction and H_2 Reduction
5,202,549	D. D. Decker and O. J. Holder II	Transaction Machine Having Lighted User Prompts
5,202,640	R. L. Schaaf, F. W. Olsen, and E. J. Tasillo	Capacitance and Leakage Test Method and Apparatus
5,202,671	M. A. Aranda, Y. Iida, and A. Kamel	Pick Function Implementation in a Parallel Processing System
5,202,754	C. L. Bertin, P. A. Farrar, Sr., H. L. Kalter, G. A. Kelley, Jr., W. B. van der Hoeven, and F. R. White	Three-Dimensional Multichip Packages and Methods of Fabrication
5,202,799	S. R. Hetzler, J. M. Menon, and M. F. Mitoma	Logical Data Tracks Extending among a Plurality of Zones of Physical Tracks of One or More Disk Devices
5,202,803	T. R. Albrecht, J. S. Foster, and A. M. Homola	Disk File with Liquid Film Head-Disk Interface
5,202,875	H. J. Rosen, K. A. Rubin, G. T. Sincerbox, T. C. Strand, and J. M. Zavislan	Multiple Data Surface Optical Data Storage System
5,202,889	A. Aharon, A. Bar-David, R. Gewirtzman, E. Gofman, M. Leibowitz, and V. Shwartzburd	Dynamic Process for the Generation of Biased Pseudo-Random Test Patterns for the Functional Verification of Hardware Designs
5,202,921	A. Herzberg, S. Kutten, and M. M. Yung	Method and Apparatus for Authenticating Users of a Communication System to Each Other
5,202,936	Y. Kobiyama	Method for Generating a Gray-Scale Pattern
5,202,940	R. Betts	Modular Electro-Optic Bus Coupler System
5,202,943	G. R. Carden, D. H. Danovitch, E. M. Foster, and W. W. Vetter	Optoelectronic Assembly with Alignment Member

5,202,971	L. W. Henson, A. A. Shaheen-Gouda, and T. A. Smith	System for File and Record Locking between Nodes in a Distributed Data Processing Environment Maintaining One Copy of Each File Lock
5,202,972	R. J. Gusefski, C. I. Lei, and A. Ramirez	Store Buffer Apparatus in a Multiprocessor System
5,202,981	F. W. Shackelford	Process and Apparatus for Manipulating a Boundless Data Stream in an Object Oriented Programming System
5,202,995	J. K. P. O'Brien	Method for Removing Invariant Branches from Instruction Loops of a Computer Program
5,202,998	A. G. Yanes	Fast, Simultaneous Multi-Processor System Status Communication Interface
5,203,007	P. W. Gallagher	Overriding Programmable Priority and Selective Blocking in a Computer System
5,203,075	C. G. Angulas, P. T. Flynn, J. Funari, T. E. Kindl, and R. L. Orr	Method of Bonding Flexible Circuit to Circuitized Substrate to Provide Electrical Connection Therebetween Using Different Solders
5,203,955	A. Viehbeck, S. L. Buchwalter, J. J. Glenning, M. J. Goldberg, C. A. Kovac, L. C. Matthew, W. P. Pawlowski, and S. L. Tisdale	Method for Etching an Organic Polymeric Material
5,204,138	S. V. Nguyen, D. M. Dobuzinsky, D. J. Dopp, and D. L. Harmon	Plasma Enhanced CVD Process for Fluorinated Silicon Nitride Films
5,205,035	D. W. DiAngelo, C. T. Rogers, and J. T. Taylor	Low Cost Pin and Tab Assembly for Ceramic and Glass Substrates
5,205,461	H. R. Bickford, R. R. Horton, I. C. Noyan, M. J. Palmer, and J. C. Zyzo	Method and Apparatus for Fluxless Solder Bonding
5,205,738	H. R. Anderson, Jr., A. Bross, J. G. Cempa, R. O. Lussow, D. E. Myers, and T. J. Walsh	High Density Connector System
5,205,740	R. F. Frankeny, K. Hermann, R. E. Hunt, and V. E. Whitehead	Super Connector for Connecting Flat Ribbon Cables
5,205,750	R. E. Darrow and D. E. Engle	Temperature Compensating Strain Relief Connection for Flexible Electrical Circuits
5,206,074	C. R. Davis, A. A. Mehta, K. Papathomas, L. P. Springer, W. J. Summa, and D. W. Wang	Adhesives on Polyimide Films and Methods of Preparing Them
5,206,213	J. J. Cuomo, C. R. Guarnieri, E. S. Machlin, R. A. Roy, and D. S. Yee	Method of Preparing Oriented, Polycrystalline Superconducting Ceramic Oxides
5,206,516	J. H. Keller and D. K. Coultas	Low Energy, Steered Ion Beam Deposition System Having High Current at Low Pressure
5,206,544	C.-L. Chen, R. H. Dennard, and H. I. Hanafi	CMOS Off-Chip Driver with Reduced Signal Swing and Reduced Power Supply Disturbance
5,206,583	J. W. Dawson, G. A. DeLuca, and M. Nicewicz	Latch Assisted Fuse Testing for Customized Integrated Circuits
5,206,590	B. Dieny, B. A. Gurney, S. E. Lambert, D. Mauri, S. S. P. Parkin, V. S. Speriosu, and D. R. Wilhoit	Magnetoresistive Sensor Based on the Spin Valve Effect
5,206,633	W. N. Zalph	Self Calibrating Brightness Controls for Digitally Operated Liquid Crystal Display System
5,206,769	J. A. Bailey and G. F. Gooding	Method for Controlling a Plurality of Phase-Lock Loops from a Common Frequency Control
5,206,778	D. M. Flynn and P. M. Ippolito	Sense Circuit for On-Chip Thermal Shutdown
5,206,792	S. D. Reynolds	Attachment for Contacting a Heat Sink with an Integrated Circuit Chip and Use Thereof
5,206,818	P. A. Speranza	Fugitive Emissions Monitoring System Including Integrated Fugitive Emissions Analyzer and Source Identifier
5,206,860	P. J. Brown, M. J. Kalos, and M. W. Sachs	Recovery from a Possible Switching Error in a Computer I/O System
5,206,861	N. P. Hannon, A. Tarolli, and P. L. Wiltgen	System Timing Analysis by Self-Timing Logic and Clock Paths
5,206,930	H. Ishikawa and C. Sawada	Method of Compliance Control of a Manipulator
5,206,933	J. K. Farrell, J. S. Gordon, D. C. Kuhl, T. V. Lee, and T. E. Parker	Data Link Controller with Channels Selectively Allocatable to Hyper Channels and Hyper Channel Data Funneled Through Reference Logical Channels
5,206,936	H. C. Holland, R. J. Kammerer, and R. S. Svec	Apparatus for Exchanging Channel Adapter Status among Multiple Channel Adapters
5,206,941	R. G. Eikill, C. P. Geer, and S. B. Levenstein	Fast Store-Through Cache Memory
5,207,372	J. Funari and R. J. Moore	Method for Soldering a Semiconductor Device to a Circuitized Substrate
5,207,437	M. S. Barnes, D. K. Coultas, J. C. Forster, and J. H. Keller	Ceramic Electrostatic Wafer Chuck

5,207,467	T. M. Smith	Monitor for Detecting the Absence of an Electronic Component on a Vacuum Pickup
5,207,548	S. Suffel	Wafer Transfer Apparatus
5,207,585	H. P. Byrnes, J.-M. Halbout, M. R. Scheuermann, and E. Shapiro	Thin Interface Pellicle for Dense Arrays of Electrical Interconnects
5,208,067	C. R. Jones and R. A. Susko	Surface Modification of Organic Materials to Improve Adhesion
5,208,068	C. R. Davis and R. D. Goldblatt	Lamination Method for Coating the Sidewall or Filling a Cavity in a Substrate
5,208,170	E. Kobeda and G. L. Patton	Method for Fabricating Bipolar and CMOS Devices in Integrated Circuits Using Contact Metallization for Local Interconnect and Via Landing
5,208,421	R. J. Lisle, D. J. Moore, S. C. Penn, and M. D. Wilkes	Method and Apparatus for Audio Editing of MIDI Files
5,208,512	J. C. Forster, W. M. Holber, and J. S. Logan	Scanned Electron Cyclotron Resonance Plasma Source
5,208,648	J. S. Batchelder, P. C. D. Hobbs, M. A. Taubenblatt, and D. W. Cooper	Apparatus and a Method for High Numerical Aperture Microscopic Examination of Materials
5,208,714	E. V. Denison, V. N. Kahwaty, G. C. Stern, G. Steving, and R. P. Zammit	Magnetic Hybrid Interleaved Head with Closure Supporting Islands
5,208,729	T. M. Cipolla, P. W. Coteus, G. W. Johnson, and L. S. Mok	Multi-Chip Module
5,208,772	J. P. Kasold and C. H. Lam	Gate EEPROM Cell
5,208,834	L. J. Frederickson	Lexicographical Encoding and Decoding of State-Dependent Codes
5,208,879	A. R. Gallo, G. J. Robbins, and R. R. Shaw	Optical Signal Distribution System
5,208,908	R. B. Harrison, D. C. Haigh, and R. T. Wood	Display System Having a Font Cache for the Temporary Storage of Font Data
5,208,909	J. Corona, D. G. Gibbons, and T. W. Lindgren	Pre-Drawing Pick Detection in a Graphics Display System
5,208,910	P. E. Higgins, T.-J. Hu, J. W. Potts, and W. J. Wyatt	Menu Driven and Method System for Informing Which Past Selections Have Caused Disabled Actions
5,209,815	R. J. Fleming, M. J. Lawson, E. J. Leonard, and B. N. Rhoads	Method for Forming Patterned Films on a Substrate
5,209,817	U. M. Ahmad, D. G. Berger, A. Kumar, S. J. LaMaire, K. B. Prasad, S. K. Ray, and K. H. Wong	Selective Plating Method for Forming Integral Via and Wiring Layers
5,210,447	R. C. Wong	Word Decoder with SBD-T _x Clamp
5,210,449	K. Nishino and Y. Tomonaga	Edge Triggered Tri-State Output Buffer
5,210,485	W. C. Kreiger and D. L. Wilder	Probe for Wafer Burn-In Test System
5,210,536	G. Furian	Data Compression/Coding Method and Device for Implementing Said Method
5,210,637	K. A. Puzey	High Speed Light Modulation
5,210,660	S. R. Hetzler	Sectored Servo Independent of Data Architecture
5,210,667	R. P. Zammit	Magnetic Recording Heads Employing Multiple Lapping Guides
5,210,686	J. M. Jernigan	Multilevel Bill of Material Processing
5,210,700	D. Tom	Automatic Delay Adjustment for Static Timing Analysis
5,210,723	M. D. Bates, A. C. Gay, R. M. West, and T. Williams	Memory with Page Mode
5,210,750	M. M. Nassehi, J. R. Muller, E. A. Zurfluh, P. A. Zafropulo, and W. K. Bux	Method and Apparatus for Distributed Queue Multiple Access in a Communication System
5,210,760	S. R. Bentley, G. W. Callsen, J. M. Karp, and S. C. West	Optimized Pointer Control System
5,210,774	J.-C. Abbiate, G. Richter, and J.-P. Vaudaux	Adaptive Equalization System and Method for Equalizing a Signal in a DCE
5,210,828	T. V. Bolan, J. A. Boston, G. A. Fax, D. J. Hanrahan, B. Laubli, D. A. Ring, A. T. Rundle, and D. J. Shippy	Multiprocessing System with Interprocessor Communications Facility
5,210,855	T. M. Bartol	System for Computer Peripheral Bus for Allowing Hot Extraction on Insertion without Disrupting Adjacent Devices
5,210,869	M. L. Williams	Method and System for Automated Transmission of Failure of Delivery Message in a Data Processing System
5,210,870	R. I. Baum, G. A. Brent, D. H. Gibson, and D. B. Lindquist	Database Sort and Merge Apparatus with Multiple Memory Arrays Having Alternating Access
5,210,875	R. Bealkowski, J. W. Blackledge, Jr., D. S. Cronk, R. A. Dayan, S. G. Kinnear, G. D. Kovach, M. S. Palka, Jr., R. Sachsenmaier, and K. M. Zyvoloski	Initial BIOS Load for a Personal Computer System
5,211,328	J. G. Ameen, J. Funari, and R. J. Moore	Method of Applying Solder

5,212,572	M. C. Krantz and L. D. Dickson	Optical Data Storage System with Compact Holographic Polarization-Sensing or Reflectivity-Sensing Optical Storage Head
5,212,626	D. F. Bell, B. R. Pagnani, and J. R. Warnot, Jr.	Electronic Packaging and Cooling System Using Superconductors for Power Distribution
5,212,635	K. M. Ferriter	Method and Apparatus for Measurement of Manufacturing Technician Efficiency
5,212,662	D. Cocanougher, R. K. Montoye, M. Nguyenphu, and S. L. Runyon	Floating Point Arithmetic Two Cycle Data Flow
5,212,680	R. A. Toupin	Mass Storage Device Employing Array of Transducers "Cocked" in Relation to Reciprocal Movement Axis
5,212,685	G. R. Stilwell, Jr., T. B. Vojnovich, and R. L. Walker	Control Circuit for Half-Duplex/Simplex Interface in Communication System
5,212,716	F. D. Ferraiolo, J. E. Gersbach, and I. I. Novof	Data Edge Phase Sorting Circuits
5,212,787	R. B. Baker, C. J. Cavendish, and K. L. Sitze	Method and Apparatus for Accessing a Relational Database without Exiting an Object-Oriented Environment
5,212,791	R. G. Damian and M. J. Shah	Dynamic Scheduling
5,212,793	P. A. Donica, S. R. Shubert, and K. D. Schmitz	Generic Initiators
5,213,249	D. C. Long and K. Seshan	Ultrasonic Adhesion/Dehesion Monitoring Apparatus with Power Feedback Measuring Means
5,213,600	J. Greschner, G. Schmid, W. Steiner, G. Trippel, and O. Wolter	Method of Manufacturing an Optical Storage Disk
5,213,655	M. A. Leach, B. J. Machesney, and E. J. Nowak	Device and Method for Detecting an End Point in Polishing Operation
5,213,704	H. R. Anderson, Jr., R. B. Booth, L. D. David, M. O. Neisser, H. S. Sachdev, and M. A. Takacs	Process for Making a Compliant Thermally Conductive Compound
5,213,916	J. E. Cronin, P. A. Farrar, Sr., C. W. Kaanta, J. G. Ryan, and A. J. Watts	Method of Making a Gray Level Mask
5,214,242	R. M. Gallagher	Electromagnetic Interference/Radio Frequency Interference Conducting Strip
5,214,250	B. J. Cayson, J. A. Covert, S. A. Duncan, J. M. Lauffer, I. S. Mahmoud, and R. A. Schumacher	Method of Reworking Circuit Panels, and Circuit Panels Reworked Thereby
5,214,414	J. L. Levine and G. F. Russell	Cursor for LCD Displays
5,214,536	R. Prakash	Illumination Device for a Document Line Scanner
5,214,558	G. Melnyk	Chopper Drive Control Circuit
5,214,572	R. J. Cosimano, G. J. Demorat, W. F. Ott III, and R. C. Reinhart	One-Piece Electronic Cage
5,214,603	S. H. Dhong, W. Hwang, L. M. Terman, and M. R. Wordeman	Folded Bitline, Ultra-High Density Dynamic Random Access Memory Having Access Transistors Stacked Above Trench Storage Capacitors
5,214,649	H. Van As, W. Lemppenau, and E. Zurfluh	Insert/Remove Signalling in LAN Systems
5,214,652	A. J. Sutton	Alternate Processor Continuation of Task of Failed Processor
5,214,670	D. Ballatore	Equalization System and Method for Equalizing a Base-Band Telecommunication Line
5,214,695	L. R. Arnold, R. Bealkowski, J. W. Blackledge, Jr., D. S. Cronk, R. A. Dayan, D. R. Geisler, M. T. Mittelstedt, M. S. Palka, Jr., J. D. Paul, R. Sachsenmaier, K. D. Smeltzer, P. A. Woytovech, and K. M. Zyvoloski	Apparatus and Method for Loading a System Reference Diskette Image from a System Partition in a Personal Computer System
5,214,696	H. R. Keiser II, J. N. Stevens, and J. Unger	Data Processing System and Method to Produce Softcopy Book Readers Which Are Limited to Reading Only Books Published by a Specific Publisher
5,214,698	R. M. Smith, Sr., P. C. Yeh, R. J. Easter, D. B. Johnson, A. V. Le, S. M. Matyas, J. Thomas, and J. D. Wilkins	Method and Apparatus for Validating Entry of Cryptographic Keys
5,214,752	S. N. Meshkat and J. M. Ruppert	Point Placement Method for Use in a Three-Dimensional Automatic Mesh Generation System
5,214,756	S. M. Franklin and A. M. Peters	Direct Manipulation of Icons Via conversational Linking
5,214,763	B. Blaner and S. Vassiliadis	Digital Computer System Capable of Processing Two or More Instructions in Parallel and Having a Coche and Instruction Compounding Mechanism
5,214,766	L. Liu	Data Prefetching Based on Store Information in Multi-Processor Caches

5,214,779	B. A. Barker, T. R. Edel, and J. A. Stark	Variable Construct Representation Embedded in Data Stream Which References Definition for Dynamically Generating Data Used in Processing the Data Stream
5,214,783	J.-P. Lips, J.-M. Millet, and B. Naudin	Device for Controlling the Enqueuing and Dequeuing Operations of Messages in a Memory
5,215,610	N. DiPaolo, A. H. Kumar, and L. B. Wiggins	Method for Fabricating Superconductor Packages
5,215,861	P. A. Agostino, A. Herbst, and F. M. Pressman	Thermographic Reversible Photoresist
5,216,204	T. J. Dudek, E. C. Hallee, B. U. Messina, B. D. Washo, and J. A. Weiner	Static Dissipative Electrical Cable
5,216,282	D. R. Cote, D. Stanasolovich, and R. A. Warren	Self-Aligned Contact Studs for Semiconductor Structures
5,216,362	R. L. Verkuil	Contactless Technique for Measuring Epitaxial Dopant Concentration Profiles in Semiconductor Wafers
5,216,485	K. A. Bird, D. Y. Kim, S. J. Kish, J. J. Lambright, K. R. Muller, and L. D. Thorp	Advanced Via Inspection Tool (AVIT)
5,216,554	R. C. Schneider	Digital Phase Error Estimator
5,216,562	F. S. Luecke and L. D. Dickson	Multi-Beam Optical Recording System and Method
5,216,579	M. Basara, C. B. Kneale, S. A. Lucente, and J. Natoli	Rack Based Packaging System for Computers with Cable, Cooling and Power Management Module
5,216,592	G. R. Mann and C. A. Vanderbeck	System and Method for Business Process Automation
5,216,593	B. L. Dietrich and L. F. Escudero	Method and Apparatus for Discrete Activity Resource Allocation through Cardinality Constraint Generation
5,216,659	D. E. Call and J. Lewkowicz	Laser Power Calibration by Measuring Laser Drive Current at Out of Focus and In Focus Conditions
5,216,667	W. M. Chu and A. Z. Muszynski	Simultaneous Bidirectional Transceiver
5,216,670	Y. Ofek and A. Segall	Message Stripping Protocol for a Communication Network
5,217,567	D. R. Cote, D. M. Dobuzinsky, and S. V. Nguyen	Selective Etching Process for Boron Nitride Films
5,217,586	M. Datta and L. T. Romankiw	Electrochemical Tool for Uniform Metal Removal during Electropolishing
5,218,245	H. Kohler and N. Schumacher	Programmable Neural Logic Device
5,218,296	S. L. Shinde and T. K. Worthington	Method and Apparatus for Determining at Least One Characteristic of a Superconductive Film
5,218,514	D. Q. Huynh and P. R. Vadapalli	Compact High Power Personal Computer with Improved Air Cooling System
5,218,539	S. J. Elphick and R. M. Wickes	Forms Processor with Controlled Remote Revision
5,218,584	F. Gfeller	Integrated Optical Head Structure
5,218,585	E. R. Childers, T. Kokubo, D. P. McReynolds, and K. Nanke	Phase Shifting Feedback Control in an Optical Disk Drive
5,218,677	R. C. Bono, H. R. Brandt, H. F. Cavagnaro, A. E. Lee, D. W. Norton, E. T. Shalkey, D. L. Silsbee, D. S. Wehrly, C. T. Williams, and T. K. Zimmerman	Computer System High Speed Link Method and Means
5,218,680	J. K. Farrell, J. S. Gordon, D. C. Kuhl, and T. V. Lee	Data Link Controller with Autonomous in Tandem Pipeline Circuit Elements Relative to Network Channels for Transferring Multitasking Data in Cyclically Recurrent Time Slots
5,218,696	R. Baird, R. R. Berbec, G. P. Bozman, A. S. Lett, J. J. Myers, W. H. Tetzlaff, and J. H. Unger	Method for Dynamically Expanding and Rapidly Accessing File Directories
5,218,699	R. T. Brandle, D. L. Goodliffe, D. E. Keith, R. A. Robinette, R. C. Sizemore, G. J. Smithwick, and A. J. Zappavigna	Remote Procedure Calls in Heterogeneous Systems
5,218,713	W. E. Hammer, W. H. Schwane, and F. J. Ziecina	Distributed Data Management Mechanism for Handling a Data Stream
5,220,815	R. V. Davidge, J. R. Dewitt, P. J. Galinis, F. A. Kuchar, Jr., J. H. Neer, and T. H. Newsom	Device-Port Locking Covers
5,221,367	M. F. Chisholm, P. D. Kirchner, A. C. Warren, and J. M. Woodall	Strained Defect-Free Epitaxial Mismatched Heterostructures and Method of Fabrication
5,221,420	K. L. Covert, J. E. Kingsley, D. N. Light, and R. A. Schumacher	Etching Method for Increased Circuitized Line Width and Uniformity
5,221,425	G. W. Blanchard, C. R. Bossi, E. H. Payne, and T. W. Weeks	Method for Reducing Foreign Matter on a Wafer Etched in a Reactive Ion Etching Process
5,221,449	E. G. Colgan and P. M. Fryer	Method of Making Alpha-Ta Thin Films
5,221,631	H. Ikeda, O. Shimada, T. Uchida, and T. Murakami	Method of Fabricating a Thin Film Transistor Having a Silicon Carbide Buffer Layer

5,221,853	R. V. Joshi, C.-S. Oh, and D. Moy	MOSFET with a Refractory Metal Film, a Silicide Film and a Nitride Film Formed On and In Contact with a Source, Drain and Gate Region
5,221,864	D. E. Galbi and R. J. Houghton	Stable Voltage Reference Circuit with High Vt Devices
5,221,905	L. S. Bhangu, T. Morrison, and K. Probst	Test System with Reduced Test Contact Interface Resistance
5,221,907	F. W. Olsen and E. J. Tasillo	Pseudo Logarithmic Analog Step Adder
5,222,012	R. F. Frankeny, J. A. Frankeny, and T. Q. Ngo	Power Management Circuit for a Magnetic Repulsion Punch
5,222,146	L. R. Bahl, P. V. De Souza, P. S. Gopalakrishnan, and M. A. Picheny	Speech Recognition Apparatus Having a Speech Coder Outputting Acoustic Prototype Ranks
5,222,164	R. H. Bass, Sr., B. A. Hardman, S. M. Harper, H. Pasterchick, Jr., and M. L. Weisenbloom	Electrically Isolated Optical Connector Identification System
5,222,196	J. P. Bigus	Neural Network Shell for Application Programs
5,222,202	K. Koyamada	Method and Apparatus for Visualization of Iso-Valued Surfaces
5,222,214	Y. Kobayashi	Image Processing Using a RAM and Repeat Read-Modify-Write Operation
5,222,215	N. C. Chou, P. H. Gum, R. E. Hough, M. J. Kim, J. C. Mazurowski, D. W. McCauley, C. A. Scalzi, J. F. Scanlon, and L. W. Wyman	CPU Expansive Gradation of I/O Interruption Subclass Recognition
5,222,217	M. L. Blount, S. P. Morgan, K. A. V. Rader, R. K. Rader, and S.-Gouda	System and Method for Implementing Operating System Message Queues with Recoverable Shared Virtual Storage
5,222,225	R. D. Groves	Apparatus for Processing Character String Moves in a Data Processing System
5,222,229	M. Fukuda, T. Matsumoto, and T. Nakada	Multiprocessor System Having Synchronization Control Mechanism
5,222,234	D. S. Wang and F. J. Kastelic	Combining Search Criteria to Form a Single Search and Saving Search Results for Additional Searches in a Document Interchange System
5,222,238	D. M. Zobre and L. Djohan	System and Method for Shared Latch Serialization Enhancement
5,222,242	O. H. Choi, J. E. Drake, Jr., J. C. Fletcher, J. L. Harter, J. G. Knauth, D. K. Kramer, M. A. Lerner, J. L. McKinnon, L. M. Rafalow, W. E. Siddall, and M. P. Stump	System for Locating a Node Containing a Requested Resource and for Selectively Verifying the Presence of the Resource at the Node
5,222,626	J. Reinschmidt, W. Mannsdörfer, and H. Lutz	Housing for Accommodating at Least One Cassette Containing in Particular Paper Money
5,222,649	J. Funari and R. J. Moore	Apparatus for Soldering a Semiconductor Device to a Circuitized Substrate
5,222,668	R. F. Frankeny, K. Hermann, R. E. Hunt, and V. E. Whitehead	Fluid Actuated Connector
5,223,827	R. A. Bell and E. G. Britton	Process and Apparatus for Managing Network Event Counters
5,223,828	F. A. McKiel, Jr.	Method and System for Enabling a Blind Computer User to Handle Message Boxes in a Graphical User Interface
5,224,020	I. Gollledge, D. J. Gray, R. O'Regan, and F. E. Castle	Electronic Apparatus Having Modular Front and Back Functional Units and Electrical Distribution Unit Including a Fan Therebetween
5,224,039	D. R. Cox and F. M. Fandrick	Method of Enabling the Translation of Machine Readable Information
5,224,080	T. Ohtsuki	Method, Apparatus, and Medium for Magneto-Optical Recording
5,224,093	W. E. Denzel and A. J. Engbersen	High-Speed Multi-Port FIFO Buffer Circuit
5,224,098	R. F. Bird, K. H. Britton, T.-Y. D. Chung, A. K. Edwards, J. Mathew, D. P. Pozefsky, S. Sarkar, R. D. Turner, W. W. Chung, Y. T. Yeung, J. P. Gray, H. D. Dykeman, W. A. Doeringer, J. S. Auerbach, and J. H. Wilson	Compensation for Mismatched Transport Protocols in a Data Communications Network
5,224,111	G. R. Stilwell, Jr. and J. W. Sylivant	Dual Fiber Drive System
5,224,166	R. C. Hartman, Jr.	System for Seamless Processing of Encrypted and Non-Encrypted Data and Instructions
5,224,193	W. P. Risk	First Order Mode Frequency Doubler System and Method
5,224,205	J. E. Dinkin, J. L. Harter, J. M. Henderson, D. K. Kramer, M. A. Lerner, and H. J. Sandick	Method of Combining Architecturally Dissimilar Computing Networks into a Single Logical Network
5,224,213	J. N. Dieffenderfer and R. N. Kalla	Ping-Pong Data Buffer for Transferring Data from One Data Bus to Another Data Bus
5,224,215	J. R. Disbrow	Message Queue Processing among Cooperative Processors Having Significant Speed Differences
5,224,265	J. B. Dux, J. L. Poetzinger, R. M. Prestipino, and K. L. Siefering	Fabrication of Discrete Thin Film Wiring Structures
5,224,417	E. F. Helinski	Interposer Device and Striker Strip Assembly Therefor

5,225,711	C.-A. Chang, N. G. Koopman, J. M. Roldan, S. Strickman, K. K. Srivastava, and H. L. Yeh	Palladium Enhanced Soldering and Bonding of Semiconductor Device Contacts
5,225,777	A. Bross and T. J. Walsh	High Density Probe
5,225,888	G. S. Selwyn and R. E. Walkup	Plasma Constituent Analysis by Interferometric Techniques
5,225,926	J. J. Cuomo, C. R. Guarnieri, B. A. Hoenig, H. Seki, J. L. Speidell, and S. J. Whitehair	Durable Optical Elements Fabricated from Free Standing Polycrystalline Diamond and Non-Hydrogenated Amorphous Diamond Like Carbon (DLC) Thin Films
5,225,971	J. H. Spreen	Three Coil Bridge Transformer
5,225,991	D. K. Dougherty	Optimized Automated Macro Embedding for Standard Cell Blocks
5,226,041	J. G. Wacławsky and K. L. Marshall	Method for Efficiently Simulating the Dynamic Behavior of a Data Communications Network
5,226,079	C. J. G. Holloway	Non-Repudiation in Computer Networks
5,226,117	S. J. Miklos	Method for Simultaneous Update and Change in Parent and Child Windows
5,226,134	A. Aldereguia, D. C. Cromer, and R. M. Stutes	Data Processing System Including a Memory Controller for Direct or Interleave Memory Accessing
5,226,143	R. Baird, G. P. Bozman, A. S. Lett, J. J. Myers, and W. H. Tetzlaff	Multiprocessor System Includes Operating System for Notifying Only Those Cache Managers Who Are Holders of Shared Locks on a Designated Page by Global Lock Manager
5,226,144	A. Moriwaki and S. Shimizu	Cache Controller for Maintaining Cache Coherency in a Multiprocessor System Including Multiple Data Coherency Procedures
5,226,156	N. H. MacLean, Jr., R. W. Miller, and M. R. Patel	Control and Sequencing of Data Through Multiple Parallel Processing Devices
5,226,158	G. R. Horn and T. R. Malkemus	Method and Apparatus for Maintaining Referential Integrity within a Relational Database
5,226,159	L. W. Henson, D. W. Johnson, S. P. Morgan, and T. A. Smith	File Lock Management in a Distributed Data Processing System
5,226,162	R. H. Daw	Assist Program for Operating a Debug Program in Conjunction with a User Program
5,226,164	S. J. Nadas and R. J. Pedersen	Millicode Register Management and Pipeline Reset
5,226,169	S. L. Gregor	System for Execution of Storage-Immediate and Storage-Storage Instructions within Cache Buffer Storage
5,226,732	J. S. Nakos, P. E. Bakeman, Jr., D. P. Hallock, J. B. Lasky, and S. L. Pennington	Emissivity Independent Temperature Measurement Systems
5,227,010	J. S. Lubert and D. M. McGarigle	Regeneration of Ferric Chloride Etchants
5,227,212	R. H. Ahlert, J. K. Howard, S. E. Lambert, and I. L. Sanders	Magnetic Recording Disk, Magnetoresistive Read Head, Inductive Write Head Combination
5,227,280	J. A. Jubinsky, S. M. Katz, C. F. Lyons, and W. M. Moreau	Resists with Enhanced Sensitivity and Contrast
5,227,330	P. D. Agnello, T.-S. Kuan, and T. O. Sedgwick	Comprehensive Process for Low Temperature Si Epit Axial Growth
5,227,333	J. F. Shepard	Local Interconnection Having a Germanium Layer
5,227,338	B. R. Kryzaniwsky	Three-Dimensional Memory Card Structure with Internal Direct Chip Attachment
5,227,625	S. R. Hetzler	Retro-Reflective Variable-Pitch Diffraction Grating for Use in a Rotary Actuator Disk File
5,227,658	K. D. Beyer, S.-M. Ku, V. J. Silvestri, and A. S. Yapsir	Buried Air Dielectric Isolation of Silicon Islands
5,227,676	A. A. Bahr and T. R. Larson	Current Mode Sample-and-Hold Circuit
5,227,677	A. Furman	Zero Power Transmission Line Terminator
5,227,893	A. H. Ett	Pseudo-Bar Code Control of Image Transmission
5,227,929	L. D. Comerford	Portable Computer Hard Disk Protective Reflex System
5,227,995	E. Klink, H. Kohler, and H. Pross	High Density Semiconductor Memory Module Using Split Finger Lead Frame
5,228,046	R. M. Blake, D. C. Bossen, C.-L. Chen, J. A. Fifield, and H. L. Kalter	Fault Tolerant Computer Memory Systems and Components Employing Dual Level Error Correction and Detection with Disablement Feature
5,228,115	K. S. Natarajan	Hybrid Backtrack/Lookahead Search Technique for Constraint-Satisfaction Problems
5,228,120	R. W. E. Farr and D. C. Haigh	Display System with Direct Color Mode
5,228,122	R. S. Cahn and E. B. Sherwin	Method for Bypassing User Unwanted Display Screens from a Host Controlled Terminal
5,228,136	S. Shimizu and M. Ohara	Method and Apparatus to Maintain Cache Coherency in a Multiprocessor System with Each Processor's Private Cache Updating or Invalidating Its Contents Based Upon Set Activity
5,228,502	R. C. Chu, G. F. Goth, G. P. Messina, K. P. Moran, and M. L. Zumbrennen	Cooling by Use of Multiple Parallel Convective Surfaces
5,228,862	J. G. Baumberger and J. R. Petrozello	Fluid Pressure Actuated Connector

5,228,863	J. S. Campbell and D. B. Howe	Connection Device for Use in an Electrical Circuitry System
5,229,251	E. D. Babich, J. D. Gelorme, R. W. Nunes, S. L. Nunes, J. R. Paraszczak, and R. J. Serino	Dry Developable Photoresist Containing an Epoxide, Organosilicon and Onium Salt
5,229,256	H. Ito	Process for Generating Positive-Tone Photoresist Image
5,229,322	S.-F. S. Chu, K.-M. Kim, S.-N. Mei, M. J. Saccamango, D. R. Vigliotti, and R. J. von Gutfeld	Method of Making Low Resistance Substrate or Buried Layer Contact
5,229,328	M. F. Bregman, R. R. Horton, A. P. Lanzetta, I. C. Noyan, M. J. Palmer, and H.-M. Tong	Method for Bonding Dielectric Mounted Conductors to Semiconductor Chip Contact Pads
5,229,550	P. S. Bindra, D. A. Canfield, V. R. Markovich, J. McKeveny, R. E. Ruane, and E. L. Thomas	Encapsulated Circuitized Power Core Alignment and Lamination
5,229,593	R. T. Cato	Apparatus and Method for Safe, Free Space Laser Communication
5,229,639	K. M. Hansen and D. J. Perlman	Low Powder Distribution Inductance Lead Frame for Semiconductor Chips
5,229,855	J. I. Siann	System and Method for Combining Multiple Composite Video Signals
5,229,899	C. A. Brown, T. A. Gregory, C. G. Keller, H. R. Wendt, and A. R. Zingher	Apparatus and Method for Controlling Vapor Phase within an Enclosure
5,229,916	R. F. Frankeny and R. L. Imken	Chip Edge Interconnect Overlay Element
5,230,037	M. Giustiniani and P. Pierucci	Phonetic Hidden Markov Model Speech Synthesizer
5,230,041	J. M. Dinwiddie, Jr., B. J. Freeman, G. A. Suarez, and B. J. Wilkie	Bus Interface Circuit for a Multimedia System
5,230,047	A. H. Frey, Jr. and R. C. Mosteller	Method for Balancing of Distributed Tree File Structures in Parallel Computing Systems to Enable Recovery After a Failure
5,230,049	P. Y. Chang, D. J. Coyle, Jr., D. C. Hargrove, and D. S. Hidalgo	Program Source Code Translator
5,230,052	R. A. Dayan, R. Sachsenmaier, and S. Y. Yen	Apparatus and Method for Loading BIOS into a Computer System from a Remote Storage Location
5,230,055	N. A. Katz, R. F. Pollitt, L. L. Suarez, and C. W. Frank	Battery Operated Computer Operation Suspension in Response to Environmental Sensor Inputs
5,230,069	D. P. Brelsford, M. M. Cutler, J.-L. Lafitte, J. M. Gdaniec, D. L. Osisek, and K. E. Plambeck	Apparatus and Method for Providing Private and Shared Access to Host Address and Data Spaces by Guest Programs in a Virtual Machine Computer System
5,230,070	L. Liu	Access Authorization Table for Multi-Processor Caches
5,230,074	F. J. Canova, Jr., N. A. Katz, R. F. Pollitt, L. L. Suarez, S. Astarabadi, and C. W. Frank	Battery Operated Computer Power Management System